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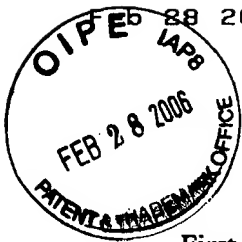
TO:	FACSIMILE No.:	TELEPHONE No.:
Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Arlington, VA 22313-1450	(571) 273-8300 (571) 273-2885	() -
ATTENTION:	Examiner: <u>Hong Chong KIM</u>	
	Art Unit: <u>2185</u>	

FROM:		TELEPHONE No.:
Michael J. Ure, Reg. No. 33,089		(408) 474-9077
RE:	Serial No.: <u>09/829,793</u>	
	Attorney Docket No.: <u>NL 000215</u>	

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First-Named Inventor: Martijn Johannes
Lambertus EMONS
Application No.: 09/829,793 Conf.: 1893
Date Filed: 04/10/2001
Notice of Allowance Date: 11/30/2005

Docket No.: NL 000215
Art Unit: 2185
Examiner: Hong Chong KIM

Title: CACHE INTERFACE CIRCUIT FOR AUTOMATIC CONTROL OF CACHE BYPASS
MODES AND ASSOCIATED POWER SAVINGS

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